



Patent No.:

LGS/P-175

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

#10/000

In re Application of

: **OFFICIAL DRAFTSMAN**

Ho Hyun KIM

: Allowed: February 22, 2002

Serial No.: 09/281,973

: Group Art Unit: 2133

Confirm. No.: 7981

: Examiner: P. Chung

Filed: March 31, 1999

For: **TEST MODE SETUP CIRCUIT FOR MICROCONTROLLER UNIT**

**LETTER SUBMITTING FORMAL DRAWINGS**

Assistant Commissioner for Patents  
Washington, D. C. 20231

Sir:

In response to the Notice of Allowability dated February 22, 2002, submitted herewith is one (1) sheet of formal drawings (Figure 1) in connection with the above-identified application.

Respectfully submitted,  
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**OK to Enter**

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